CORE OF HIGH-PERFORMANCE PROCESSOR WITH HIGH-LEVEL INNER LANGUAGE

Areas of Application

The high-performance processor core is a microprocessor electronic component based on programmable logic device to be used for creating advanced sonar, communication, and navigation systems for defense and civil purposes

Specification

Fixed bitness of 24/48 binary bits;
field command structure;
super von Neumann architecture;
reducing parallelism as method
of computing organization;
three-address data addressing;
fixed 96-bit instruction format;
depth of nested loops is 32;
speed is 100 megaflops

High-performance processor core based on programmable logic device

Advantages

There no counterparts in Ukraine.
The high-level inner language and merger of data computations with predicate computations into one command provides reducing length of program code and lowering costs of applied program development

Stage of Development. Suggestions for Commercialization

IRL7, TRL5
Manufacture of small series, delivery, warranty service, and staff training are provided upon request

IPR Protection

IPR1, IPR2

Contact Information

Sergii V. Yershov, Glushkov Institute of Cybernetics of the NAS of Ukraine;
+38 044 526 41 78, e-mail: ErshovSV@nas.gov.ua